

保管单位 Storage	SA System
保存年限 Retention Period	3 years

TFT-LCD Module Specification

ver.0.0

(Light Source: LED)

Model Name:	CE21M21A
Part Number:	XXXXXXXXXX
Issued Date: (MM/DD/YYYY)	08/15/2012

	Item	Name	Date(MM/DD/YYYY)	Signature
	Prepared	冷文龙	08/15/2012	
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	Approved	朱德涛	08/15/2012	
Customer:				
Vendor Approval Signature			Date	

-CONTENTS -

REVISION HISTORY	3
1. GENERAL DESCRIPTION	4
1.1 OVERVIEW	4
1.2 CHARACTERISTICS	4
1.3 MECHANICAL SPECIFICATIONS	4
2. ABSOLUTE MAXIMUM RATINGS	4
2.1ABSOLUTE RATINGS OF ENVIRONMENT	4
3. ELECTRICAL CHARACTERISTICS	5
3.1 ABSOLUTE MAXIMUM RATING	5
3.2 CONTROL CIRCUIT DRIVING	5
3.3 LED LIGHTBAR SPECIFICATION FOR BACKLIGHT	7
4. INTERFACE PIN CONNECTION	7
4.1 TFT LCD MODULE	7
4.2 BLOCK DIAGRAM (TFT LCD MODULE)	9
4.3 LVDS INTERFACE	9
4.4 COLOR DATA INPUT ASSIGNMENT	11
4.5 BACKLIGHT INTERFACE CONNECTION	12
4.6 BACKLIGHT UNIT	12
5. INTERFACE TIMING	13
5.1 INPUT SIGNAL TIMING SPECIFICATIONS	13
5.2 LVDS SIGNAL CHARACTERISTICS	13
6. OPTICAL CHARACTERISTICS	15
6.1 OPTICAL SPECIFICATION	15
7. DEFINITION OF LABELS	16
7.1 MODULE LABEL	16
7.2 PACKING LABEL	16
8. PACKING	17
8.1 PACKING SPECIFICATIONS	17
8.2 PACKING METHOD	17
9. PRECAUTIONS	17
9.1 ASSEMBLY AND HANDLING PRECAUTIONS	18
9.2 SAFETY PRECAUTIONS	18
10. RELIABILITY	18
11. MECHANICAL DRAWING	18

LCD Module Spec.

保管単位 Storage	SA System
保存年限 Retention Period	3 years

Revision History

[illegible]

1. GENERAL DESCRIPTION

1.1 OVERVIEW

This module is color active matrix LCD module incorporating amorphous silicon TFT(Thin Film Transistor) LCD panel. It is composed of a color TFT-LCD panel, driver ICs, LED Backlight...etc. Graphics and texts can be displayed on a 1920×RGB×1080 dots panel with about 16.7M colors (R/G/B 6bits+Hi FRC data in each color) by using LVDS(Low Voltage Differential Signaling) to interface, +5V of DC supply voltage

1.2 CHARACTERISTICS

CHARACTERISTICSITEMS	SPECIFICATIONS
Screen Diagonal [in]	21.5"
Pixels [lines]	1920×1080
Active Area [mm]	476.64 (H) x 268.11 (V)
Pixel Pitch [mm]	0.24825 (H) x 0.24825 (V)
Pixel Arrangement	RGB vertical stripe
Display Mode	Normally White
Surface treatment (Without the protection film)	Anti-glare,3H

1.3 MECHANICAL SPECIFICATIONS

Item		Min.	Typ.	Max.	Unit	Remark
Module Size	Horizontal (H)	495.1	495.6	496.1	mm	[Note 1]
	Vertical (V)	291.7	292.2	292.7	mm	[Note 1]
	Depth (D)	9.8	10.3	10.8	mm	[Note 1]
Weight			(1750)		g	

[Note 1] Please refer to the attached drawings for more information of front and back outline dimensions and the dimension of bosses are not included.

2. ABSOLUTE MAXIMUM RATINGS

2.1ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Value		Unit	Remark
		Min.	Max.		
StorageTemperature	T _{ST}	-20	+60	℃	[Note1,3]
OperatingAmbientTemperature	T _{OP}	0	50	℃	[Note1,2,3]

Storage Condition: With shipping package.

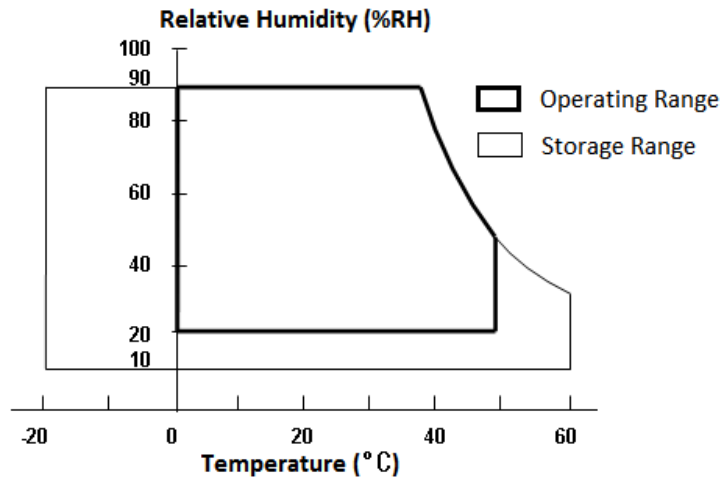
Shelf life: one year

[Note 1] Temperature and relative humidity range is shown in the figure below.

(a)90%RHMax.(Ta≤40℃).

(b)Wet-bulbtemperatureshouldbe39℃Max.(Ta>40℃).

(c) No condensation.



[Note 2] The maximum operating temperature is based on the test condition that the surface temperature of display area is less than or equal to 50°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in your product design to prevent the surface temperature of display area from being over 60°C. The range of operating temperature may degrade in case of improper thermal management in your product design.

[Note 3] The rating of environment is base on LCD module. Except LCD module, the customer has to consider the ability of other parts of LCD set and LCD set assembly process.

3. ELECTRICAL CHARACTERISTICS

3.1 ABSOLUTE MAXIMUM RATING

Parameter	Symbol	Condition	Ratings	Unit	Remark
+5V supply voltage	VCC	Ta=25°C	0 ~ +6	V	
Storage temperature	Tstg	-	-20 ~ +60	°C	
Operation temperature	Topa	-	0 ~ +50	°C	

3.2 CONTROL CIRCUIT DRIVING

Parameter		Symbol	Min	Typ	Max	Unit	Remark	
+5V supply voltage	Supply voltage	VCC	4.5	5.0	5.5	V	[Note 1]	
	Current dissipation	ICC	-	820	900	mA	VCC=5.0V,60Hz Black Pattern	
		IRush	-	-	3	A	[Note 2]	
Permissible input ripple voltage		VRP	-	-	300	mVp-p	VCC=5.0V	
Differential Input Threshold Voltage		High	VTH	-100	-	-	mV	VCM=1.2V [Note 3]
		Low	VTL	100	-	600	mV	
Input Differential Voltage		VID	100	-	600	mV		
Differential Input Common Mode Voltage		VCM	1.0	1.2	1.5	V		
Power consumption		P	-	4.1	4.5	W		

VCM: Common mode voltage of LVDS driver.

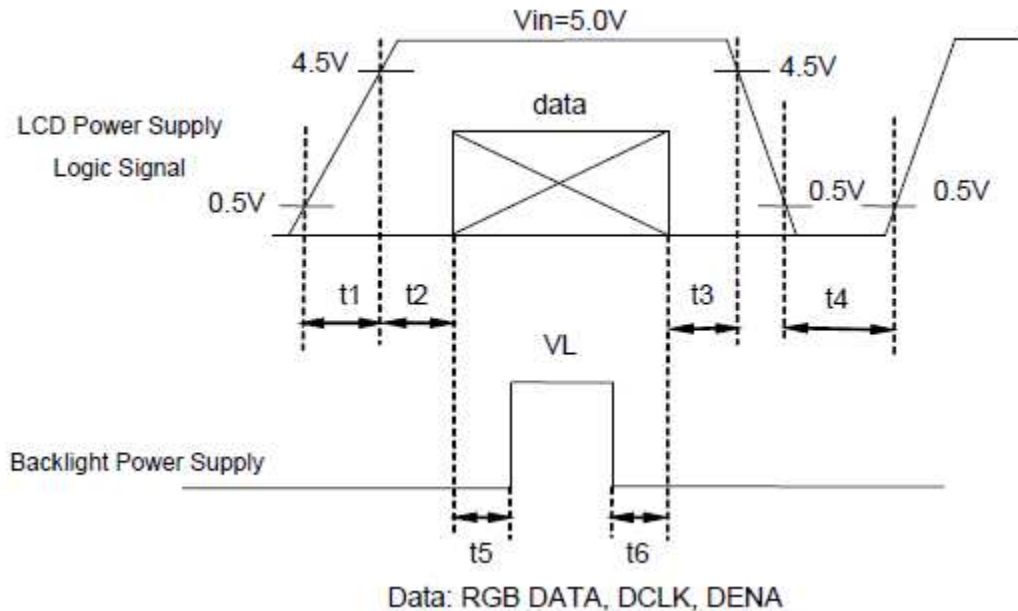
[Note1]

Power, data sequence

$0.50\text{ms} \leq t1 \leq 10\text{ms}$ $t4 \geq 1 \text{ sec}$

$0.01\text{ms} < t2 \leq 50\text{ms}$ $t5 \geq 200\text{ms}$

$0.01\text{ms} < t3 \leq 50\text{ms}$ $t6 \geq 200\text{ms}$



※ Data: CLKIN $\pm$, RIN0 $\pm$, RIN1 $\pm$, RIN2 $\pm$, RIN3 $\pm$

※ About the relation between data input and back light lighting, please base on the above-mentioned input sequence.

※ When back light is switched on before panel operation or after a panel operation stop, it may not display normally. But this phenomenon is not based on change of an incoming signal, and does not give damage to a liquid crystal display.

※ VCC-dip conditions:

(1) When $3.6\text{V} \leq \text{VCC}(\text{min}) < 4.5\text{V}$, $t_d \leq 10 \text{ ms}$

(2) When $\text{VCC} < 3.6 \text{ V}$, VCC-dip conditions should also follow the VCC-turn-on conditions.

[Note3]

VIN+: Positive differential DATA & CLK Input

VIN -: Negative differential DATA & CLK Input

3.3 LED LIGHTBAR SPECIFICATION FOR BACKLIGHT

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max		
Input Voltage	V _{pin}	42	45	48	VDC	Duty 100% [Note 1]
Input Current	I _{pin}		240		mADC	Duty 100% Per Lightbar [Note 1]
LED Life Time	LT	(30,000)			Hrs	
Power Consumption	PBL		10.8		W	
Dimming Duty Ratio		20		100	%	

[Note1] Permanent damage to the device may occur if maximum values are exceeded. Function operations should be restricted to the conditions described under Normal Operating Conditions.

[Note2] The lifetime of LED is defined as the time when it continues to operate under the condition at Ta=25±2°C and I_{pin}=240mA (Per EA)*2 until the brightness becomes ≤50% of its original value.

4. INTERFACE PIN CONNECTION

4.1 TFT LCD MODULE

CN1 (Interface signals and +5V DC power supply) Shown on the next table.

Using connector: FI-XB30SSRLA-HF-16(JAE) or compatible

Matching connector: FI-X30HL(JAE) or compatible

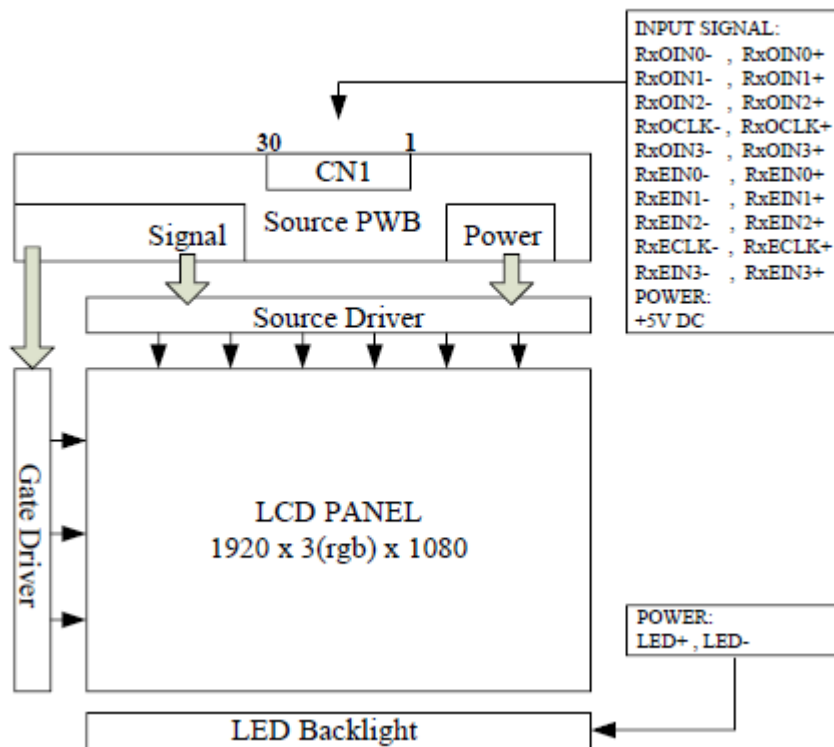
Pin No.	Symbol	Function	Remark
1	RxOIN0-	Negative LVDS DATA input(ODD)	LVDS
2	RxOIN0+	Positive LVDS DATA input(ODD)	LVDS
3	RxOIN1-	Negative LVDS DATA input(ODD)	LVDS
4	RxOIN1+	Positive LVDS DATA input(ODD)	LVDS
5	RxOIN2-	Negative LVDS DATA input(ODD)	LVDS
6	RxOIN2+	Positive LVDS DATA input(ODD)	
7	GND	Ground	LVDS
8	RxOCLK-	Negative LVDS Clock input(ODD)	LVDS
9	RxOCLK+	Positive LVDS Clock input(ODD)	LVDS
10	RxOIN3-	Negative LVDS DATA input(ODD)	LVDS
11	RxOIN3+	Positive LVDS DATA input(ODD)	LVDS
12	RxEIN0-	Negative LVDS DATA input(EVEN)	LVDS
13	RxEIN0+	Positive LVDS DATA input(EVEN)	LVDS
14	GND	Ground	
15	RxEIN1-	Negative LVDS DATA input(EVEN)	LVDS
16	RxEIN1+	Positive LVDS DATA input(EVEN)	LVDS
17	GND	Ground	
18	RxEIN2-	Negative LVDS DATA input(EVEN)	LVDS
19	RxEIN2+	Positive LVDS DATA input(EVEN)	LVDS
20	RxCLK-	Negative LVDS Clock input(EVEN)	LVDS
21	RxCLK+	Positive LVDS Clock input(EVEN)	LVDS
22	RxEIN3-	Negative LVDS DATA input(EVEN)	LVDS
23	RxEIN3+	Positive LVDS DATA input(EVEN)	LVDS
24	GND	Ground	
25	NC	No connection(Do not connect)	
26	NC	No connection(Do not connect)	
27	NC	No connection(Do not connect)	
28	VDD	POWER +5V	

LCD Module Spec.

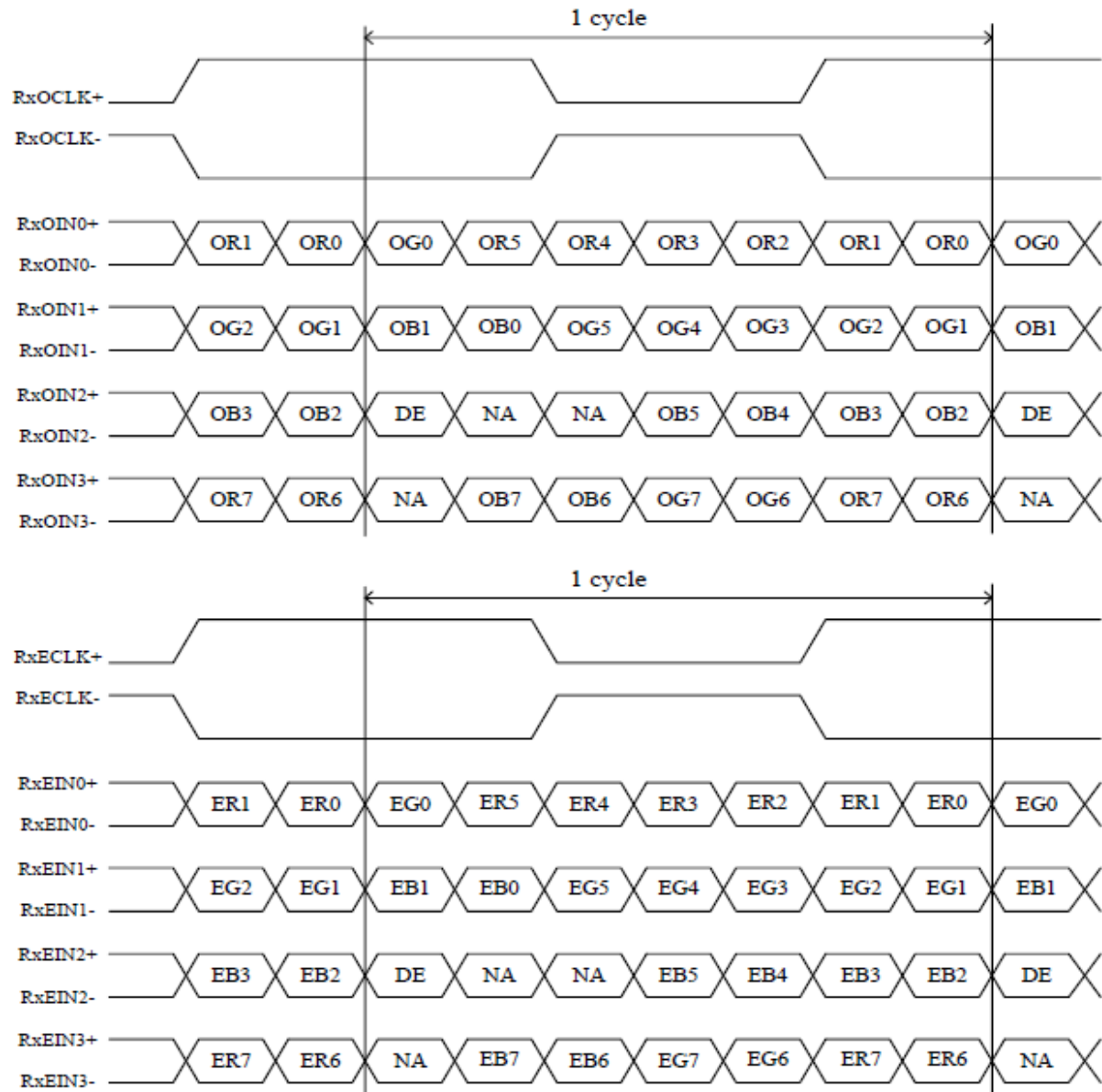
保管单位 Storage	SA System
保存年限 Retention Period	3 years

29	VDD	POWER +5V	
30	VDD	POWER +5V	

4.2 BLOCK DIAGRAM (TFT LCD MODULE)



4.3 LVDS INTERFACE



DE: Display Enable

NA: Not Available (Fixed Low)

R/G/B Data 7: MSB, R/G/B Data 0:LSB, O : "First Pixel Data" E : "Second Pixel Data"

4.4 COLOR DATA INPUT ASSIGNMENT

	Colors & Gray scale	Data signal																											
		Gray Scale	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7			
Basic Color	Black	—	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
	Blue	—	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1			
	Green	—	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0			
	Cyan	—	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1				
	Red	—	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
	Magenta	—	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1			
	Yellow	—	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0			
	White	—	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1			
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
	↑	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
	↑	↓	↓								↓								↓										
	↓	↓	↓								↓								↓										
	Brighter	GS253	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
	↓	GS254	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
	Red	GS255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Gray Scale of Green	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
	↑	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
	↑	↓	↓								↓								↓										
	↓	↓	↓								↓								↓										
	Brighter	GS253	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0			
	↓	GS254	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0			
	Green	GS255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0			
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
	↑	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0			
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0			
	↑	↓	↓								↓								↓										
	↓	↓	↓								↓								↓										
	Brighter	GS253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1			
	↓	GS254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1			
	Blue	GS255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1			

0: Low level voltage,

1: High level voltage.

Each basic color can be displayed in 256 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16,7M colors display can be achieved on the screen.

保管单位 Storage	SA System
保存年限 Retention Period	3 years

4.5 BACKLIGHT INTERFACE CONNECTION

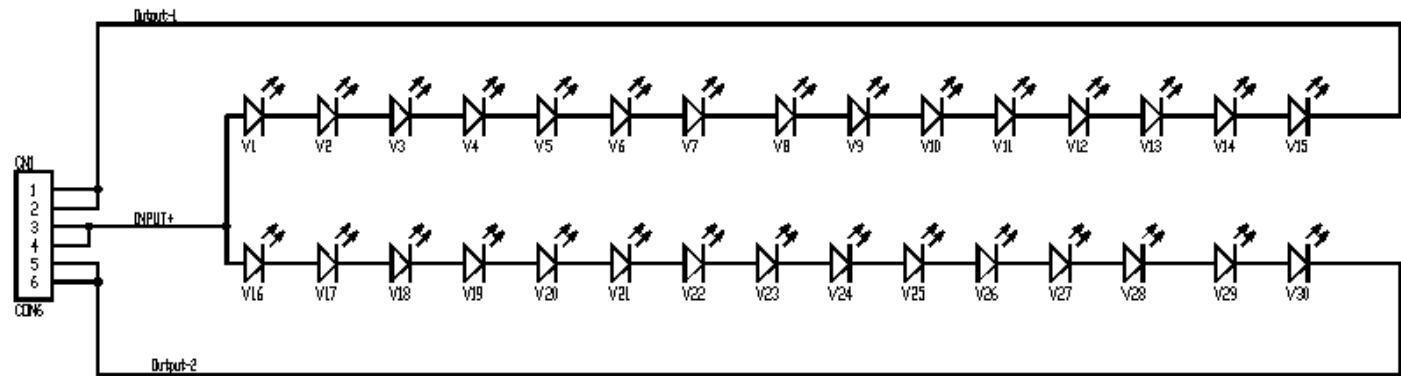
LED light bar connector type : CI1406M1HRB-NH, manufactured by CVILUX.

Pin No. Signal name

- PIN1,PIN2 ILED 1 (current out)
- PIN3,PIN4 VLED (voltage in)
- PIN5,PIN6 ILED 2 (current out)

The backlight unit contains one lightbar

Electrical Circuit of lightbar



5. INTERFACE TIMING

5.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

Item			Symbol	Min	Typ.	Max.	Unit	
LCD Timing	DCLK		Freq.	fCLK	55	72	90	MHz
			Cycle	tCLK	18.18	13.89	11.11	ns
	DENA	Horizontal	Horizontal effective time	tHA	960	960	960	tCLK
			Horizontal blank time	tHB	32	100	115	tCLK
			Horizontal total time	tH	992	1060	1075	tCLK
		Vertical	Vertical frame Rate	Fr	50	60	75	Hz
			Vertical total time	tV	1084	1130	1170	tH
			Vertical effective time	tVA	1080	1080	1080	tH
			Vertical blank time	tVB	4	50	90	tH

[Note]

*1) DENA (data enable) usually is positive.

*2) DCLK still inputs during blanking.

*3) DE mode only.

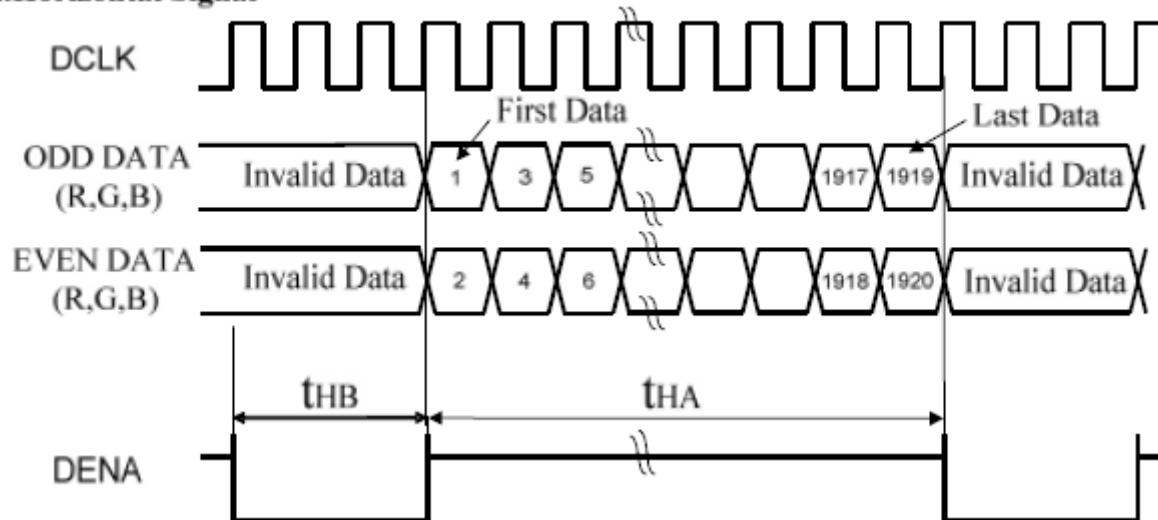
*4) It maybe cause flicker at 50Hz.

(b) Timing Chart

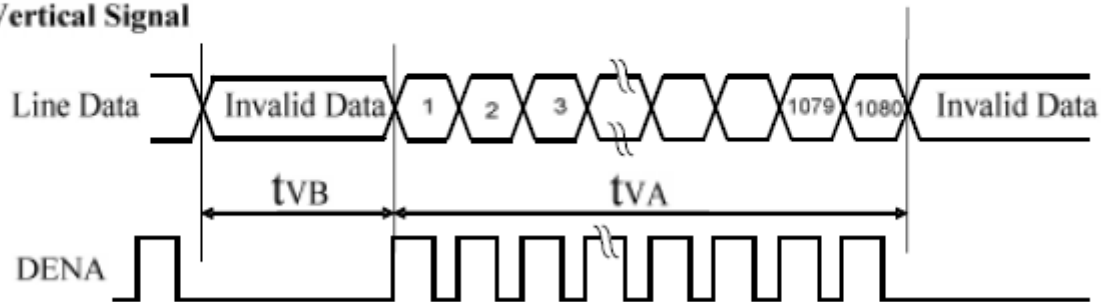
LCD Module Spec.

保管单位 Storage	SA System
保存年限 Retention Period	3 years

a.Horizontal Signal



b.Vertical Signal



6. OPTICAL CHARACTERISTICS

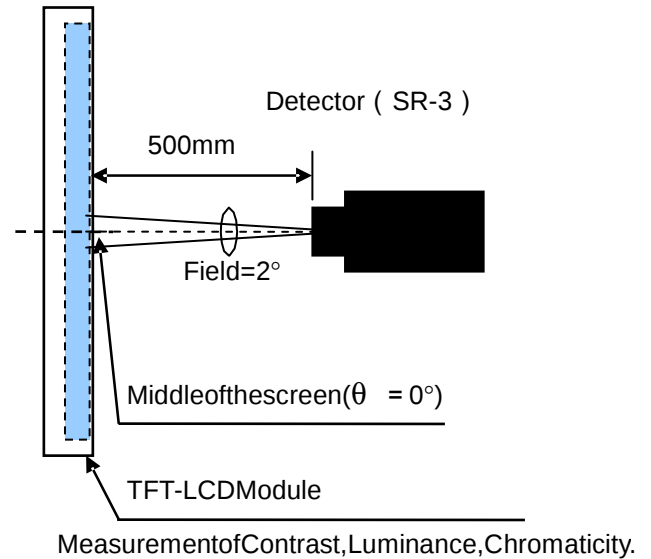
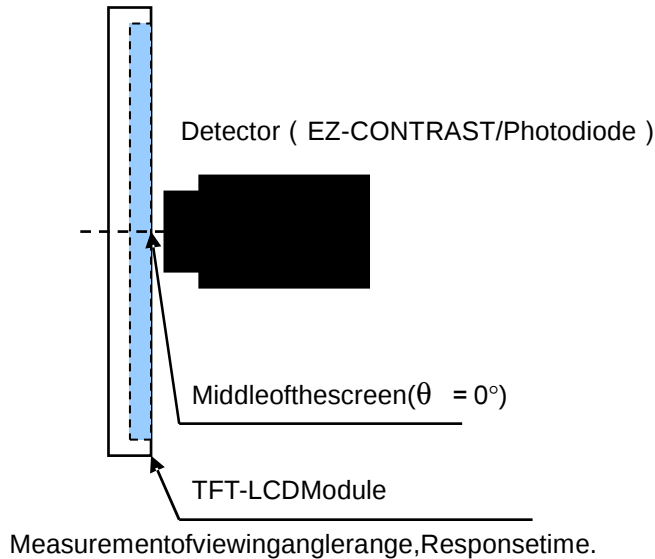
6.1 OPTICAL SPECIFICATION

Ta=25°C

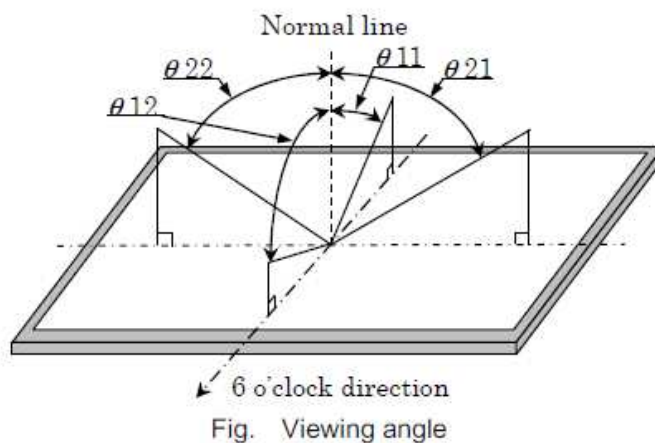
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Luminance	Central Luminance	Lwc	$\theta = 0\text{deg.}$	230	(250)		cd/m ² [Note1,4]
	Uniformity	ΔLw	$\theta = 0\text{deg.}$		TBD		% [Note1,5]
Viewing angle	Horizontal	θ_{21} θ_{22}	$CR \geq 10$		(88)	-	Deg. [Note1,4]
	Vertical	θ_{11} θ_{12}			(88)	-	Deg. [Note1,4]
Contrast ratio	CR			TBD	-	-	[Note2,4]
Response time	τ_{DRV}		-	(7)	-	ms	[Note3,4]
Chromaticity of white	x	$\theta = 0\text{deg.}$	TBD	TBD	TBD	-	[Note4]
	y			TBD		-	
Chromaticity of red	x			TBD		-	
	y			TBD		-	
Chromaticity of green	x			TBD		-	
	y			TBD		-	
Chromaticity of blue	x			TBD		-	
	y			TBD		-	
Color Gamut	C.G.		-	(68)	-	%	

*The measurement shall be executed 30 minutes after lighting at rating.

*The optical characteristics are measured using the following equipment.



[Note 1] Definitions of viewing angle range:



[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

$$\text{Contrast Ratio} = \frac{\text{Luminance (Brightness) with white screen}}{\text{Luminance (Brightness) with black screen}}$$

[Note3] Definition of response time

The response time (τ_{DRV}) is defined as the following figure and shall be measured by switching the input signal for "any level of gray (0%, 25%, 50%, 75% and 100%) and "any level of gray (0%, 25%, 50%, 75% and 100%).

LCD Module Spec.

保管单位 Storage	SA System
保存年限 Retention Period	3 years

	0%	25%	50%	75%	100%
0%		$\tau_{d:0\%-25\%}$	$\tau_{d:0\%-50\%}$	$\tau_{d:0\%-75\%}$	$\tau_{d:0\%-100\%}$
25%	$\tau_{d:25\%-0\%}$		$\tau_{d:25\%-50\%}$	$\tau_{d:25\%-75\%}$	$\tau_{d:25\%-100\%}$
50%	$\tau_{d:50\%-0\%}$	$\tau_{d:50\%-25\%}$		$\tau_{d:50\%-75\%}$	$\tau_{d:50\%-100\%}$
75%	$\tau_{d:75\%-0\%}$	$\tau_{d:75\%-25\%}$	$\tau_{d:75\%-50\%}$		$\tau_{d:75\%-100\%}$
100%	$\tau_{d:100\%-0\%}$	$\tau_{d:100\%-25\%}$	$\tau_{d:100\%-50\%}$	$\tau_{d:100\%-75\%}$	

$\tau^*:x-y$...response time from level of gray(x) to level of gray(y)

$$\tau_{DRV} = \sum (\tau^*:x-y)/20$$

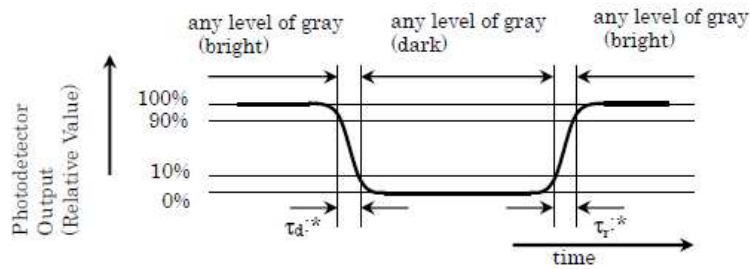


Fig. Response time

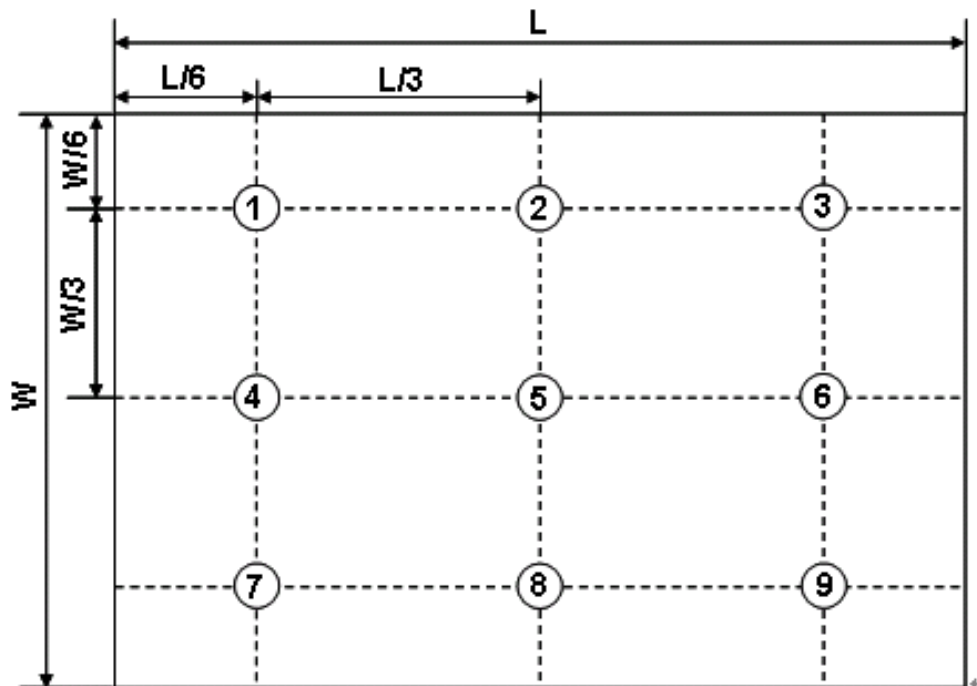
[Note 4] This shall be measured at center of the screen.

[Note 5] Definition of Luminance and Luminance uniformity:

Luminance: To measure at the center position "5" on the screen (NO.5).

Luminance uniformity: L_w (MAX) and L_w (MIN) are the maximum and minimum luminance value measure at the position "1-9" on the screen (NO.1-9) and the equation:

$$\Delta L_w = L_w(\text{MIN}) / L_w(\text{MAX}) \times 100\%$$



7. DEFINITION OF LABELS

7.1 MODULE LABEL

TBD

7.2 PACKING LABEL

TBD

8. PACKING

8.1 PACKING SPECIFICATIONS

TBD

8.2 PACKING METHOD

TBD

9. PRECAUTIONS

9.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (a) Do not apply rough force such as bending or twisting to the module during assembly.
- (b) It is recommended to assemble or to install a module into the user's system in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (c) Since the LCM consists of TFT and electronic circuits with CMOS-ICs, which are very weak to electrostatic discharge, person who is handling an LCM should be grounded through adequate methods such as an anti-static wrist band. Connector pins should not be touched directly with bare hands.

Reference: Process control standard is shown as follow,

	item	Management standard value and performance standard
1	Anti-static mat (shelf)	1 to 50 [Megaohm]
2	Anti-static mat (floor, desk)	1 to 100 [Megaohm]
3	Ionizer	Attenuate from $\pm 1000V$ to $\pm 100V$ within two seconds.
4	Anti-static wristband	0.8 to 10 [Megaohm]
5	Anti-static wristband entry and ground resistance	Below 1000 [ohm]
6	Temperature	22 to 26 [°C]
7	Humidity	60 to 70 [%]

- (d) Do not apply pressure or impulse to the module to prevent the damage of LCD panel and backlight.
- (e) Always follow the correct power-on sequence when the LCD module is turned on. This can prevent the damage and latch-up of the CMOS LSI chips.
- (f) Be sure to turn off the power supply when inserting or disconnecting the cable.
- (g) Do not disassemble the module.
- (h) Front polarizer can easily be damaged, so please pay attention on it.
- (i) Using an absorbent cotton or other soft cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (j) Since long contact with drops of water may cause discoloration or spots, please wipe off them as soon as possible.
- (k) The Panel will be broken or chipped when it is dropped or bumped against a hard substance.
- (l) Applying too much force and stress to PWBs and drivers may cause a malfunction electrically and mechanically.
- (m) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
- (n) Moisture can easily penetrate into LCD module and may cause the damage during operation.
- (o) High temperature or humidity may deteriorate the performance of LCD module. Please store LCD modules in the specified storage conditions.
- (p) This LCM is corresponded to ROHS.
- (q) When any question or issue occurs, it shall be solved by mutual discussion.

9.2 SAFETY PRECAUTIONS

- (a) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (b) After the module's end of life, it is not harmful in case of normal operation and storage.

10. RELIABILITY

(a) Environment test condition

Testitem	Condition
Hightemperaturestorageetest	Ta=60°C,240h
Lowtemperaturestorageetest	Ta=-20°C,240h
Hightemperatureandhighhumiditystorageetest	Ta=50°C,80%RH,240h (Nocondensation)
Hightemperatureoperationtest	Ta=50°C,240h
Lowtemperatureoperationtest	Ta=0°C,240h

(b) Shock & Vibration (TBD)

Testitem	Condition
Shock (Non-Operation)	Shocklevel : 50Grms Waveform : halfsinewave,11ms Direction : ±X,±Y,±Z Onetimeeachdirection
Vibration (Non-Operation)	Waveform : Random Vibrationlevel : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z,30min Eachdirectionper10min

(c) ESDtest

Testitem	Condition
Connector	200pF , 0Ω , ±250V Byusingcontact-modetodischargeeachpinonetimeandthenchecktheframe.
Module	50pF , 330Ω , ±8KV(contact-mode),±15KV(air-mode) 1.Undertestconditions,byusingair-modetodischargeeachtestpoint25times continuouslyandthenchecktheframe. 2.Undertestconditions,byusingcontact-modetodischargeeachtestpointofpanel frame25timescontinuouslyandthenchecktheframe.

[Resultevaluationcriteria]

Underthedisplayqualitytestconditionwithnormaloperationstate,thereshallbenochange,whichmay affect practicaldisplayfunction.

保管单位 Storage	SA System
保存年限 Retention Period	3 years

11. MECHANICAL DRAWING

